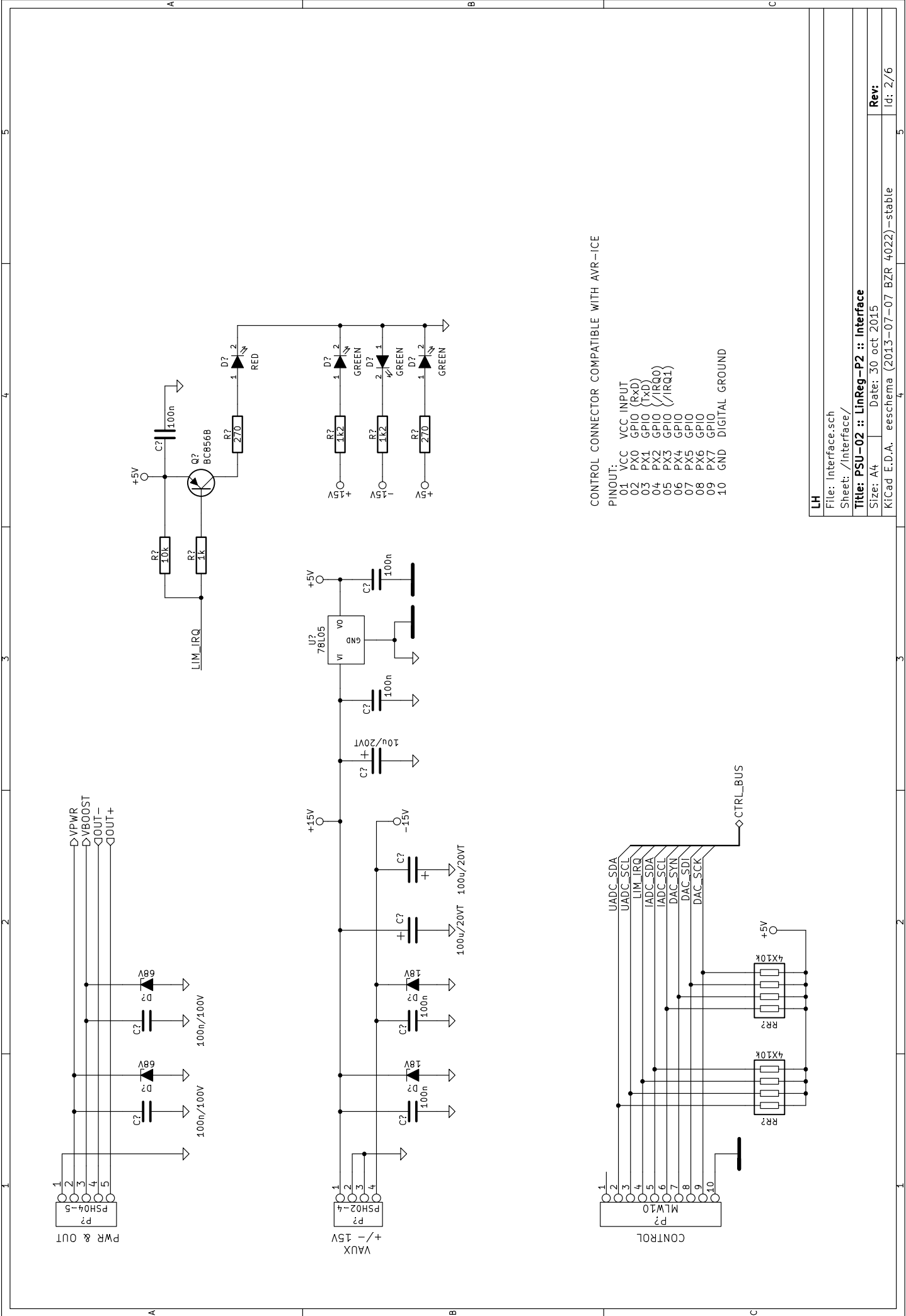
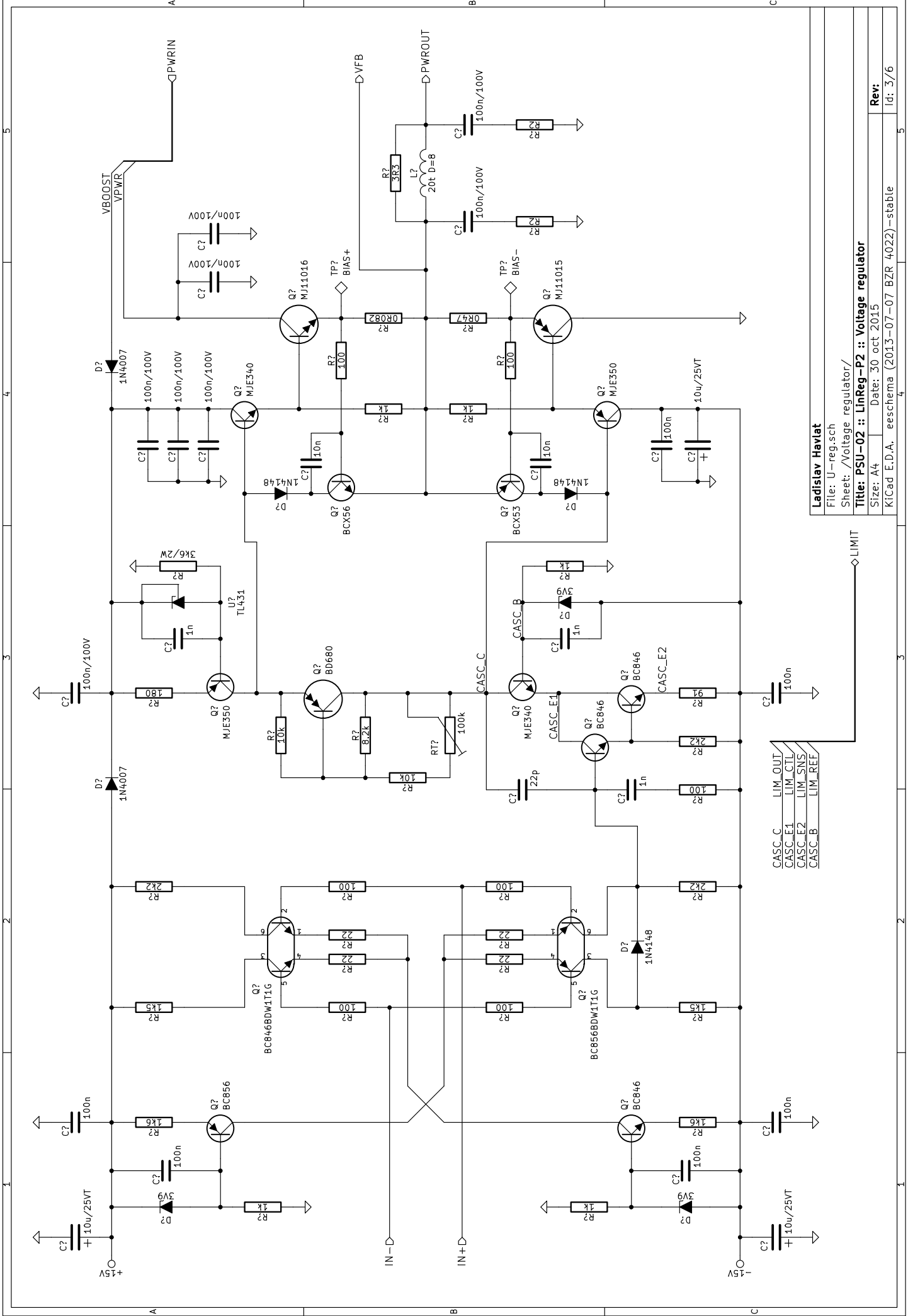






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File: U-reg.sch

Sheet: /Voltage regulator/

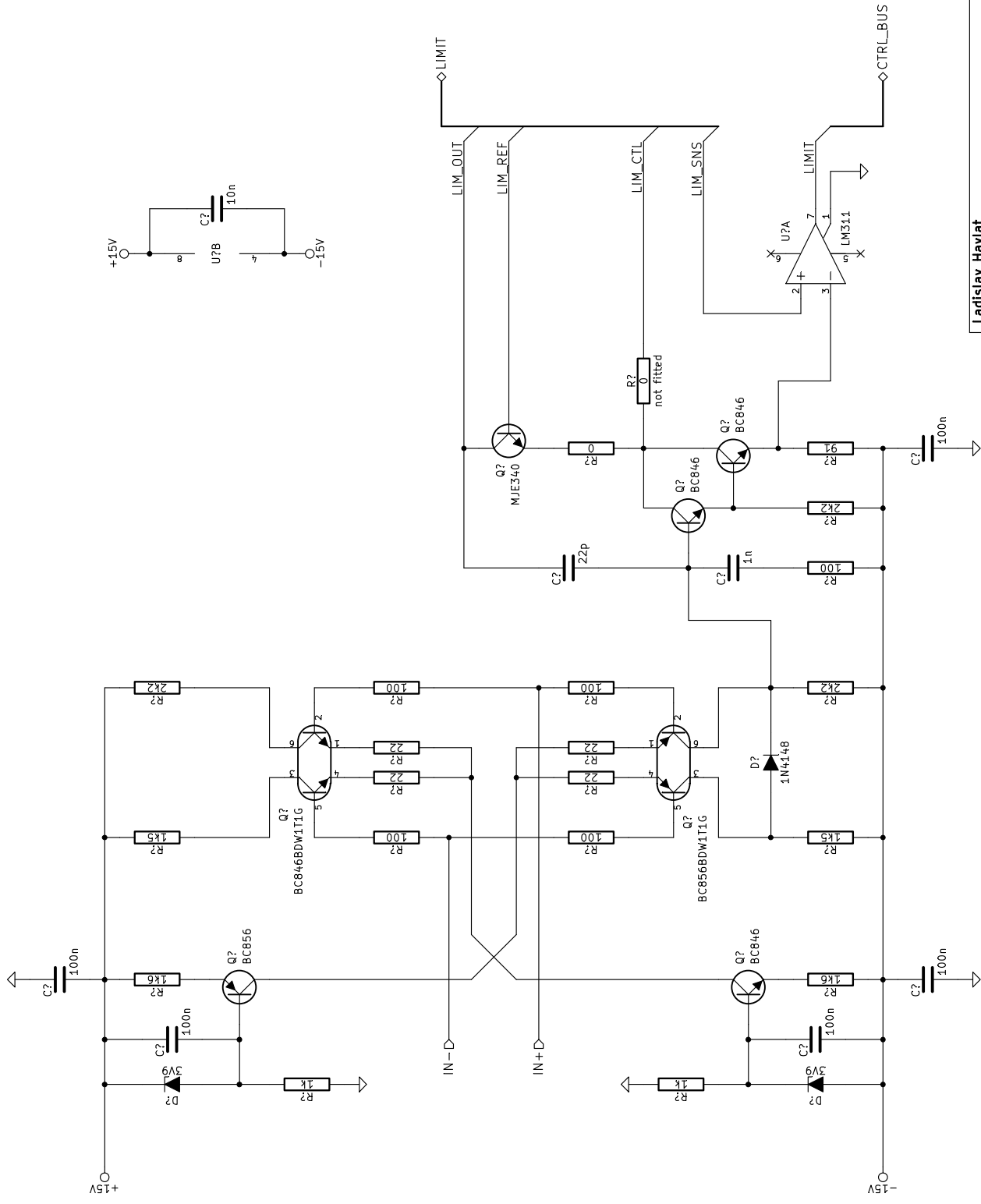
Title: PSU-02 :: LinReg-P2 :: Voltage regulator

Size: A4 Date: 30 oct 2015

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File: l-reg.sch

Sheet: /Current regulator/

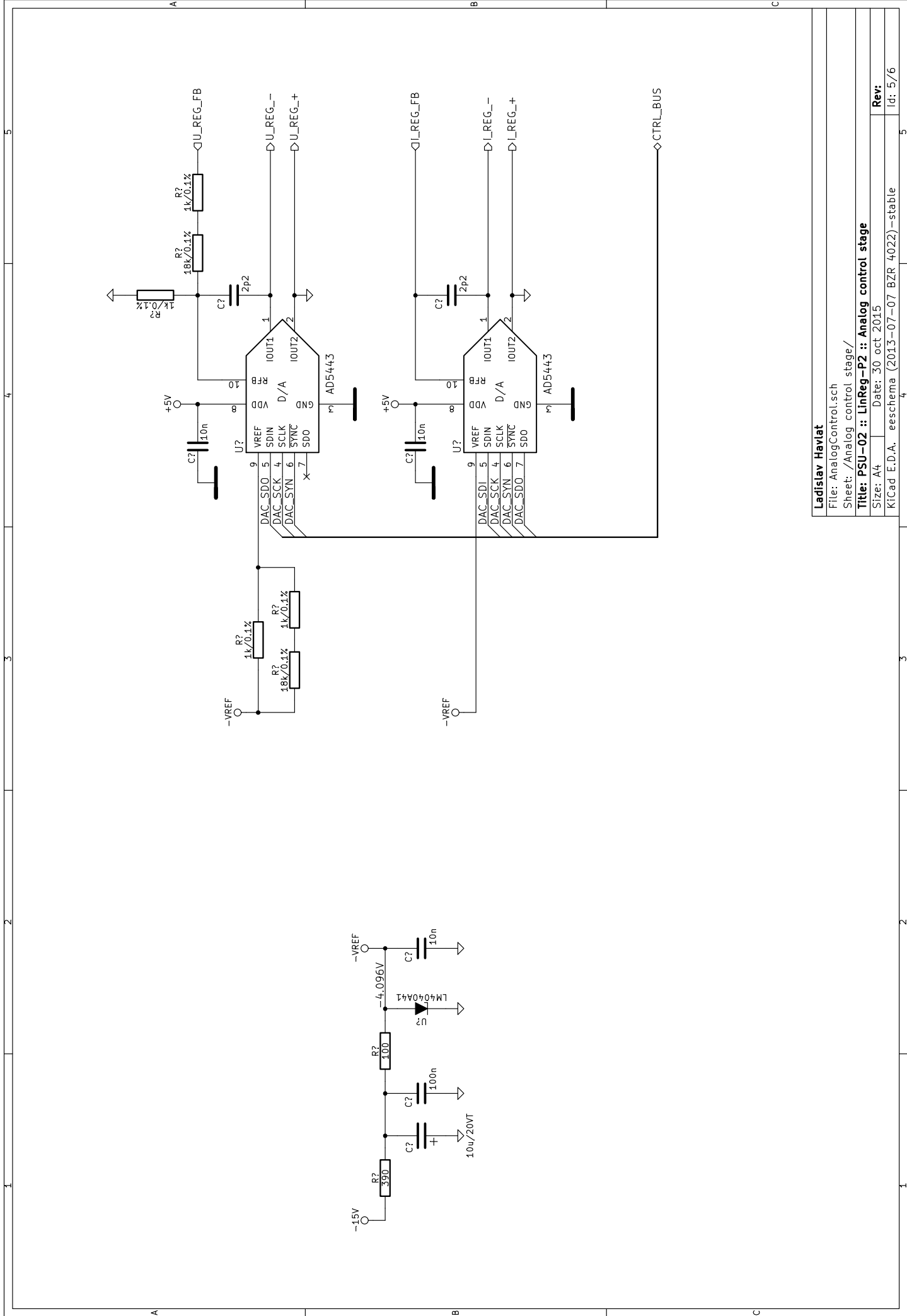
Title: PSU-02 :: LinReg-P2 :: Current Regulator

Size: A4	Date: 30 oct 2015
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KiCad E.D.A.	eesschema (2013-07-07 BZR 4022)-stable
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Rev:

Id: 4/6



Ladislav Havlat

File: AnalogControl.sch

Sheet: /Analog control stage/

Title: PSU-02 :: LinReg-P2 :: Analog control stage

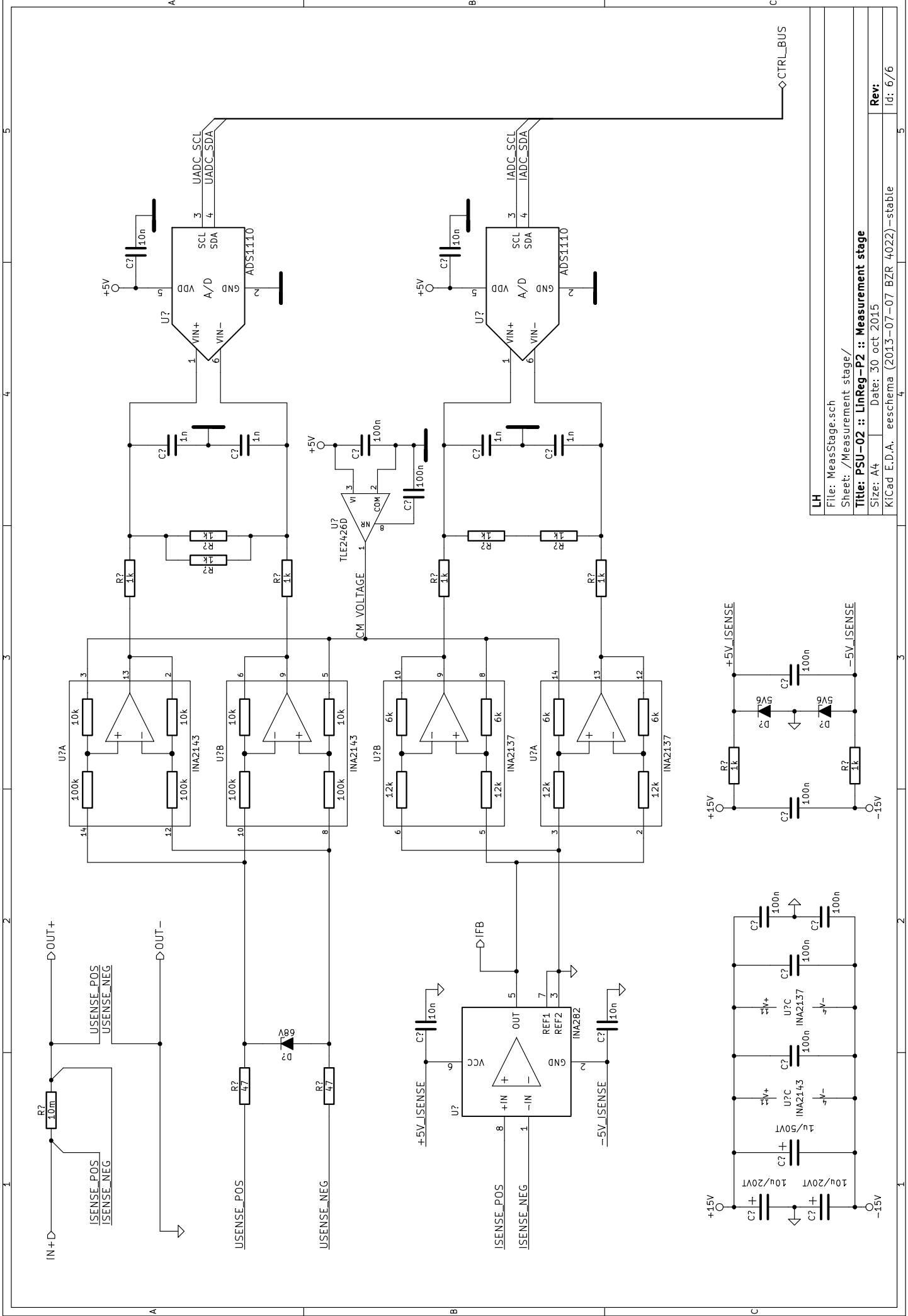
Size: A4

Date: 30 oct 2015

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

Rev:

Id: 5/6



LH	
File: MeasStage.sch	
Sheet: /Measurement stage/	
Title: PSU-02 :: LinReg-P2 :: Measurement stage	
Size: A4	Date: 30 oct 2015
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable	
Rev: 6/6	